

Stock Code: 2533.HK



BLACK SESAME TECHNOLOGIES

Corporate Introduction



“ We're going to make the chip
that changes the way we travel. ”

—— Johnson SHAN, Founder & CEO
of Black Sesame Technologies



About Black Sesame Technologies

Black Sesame Technologies is a leading provider in automotive-grade computing platform and solutions for smart vehicles. Founded in 2016, Black Sesame Technologies has listed on the main board of the Hong Kong Stock Exchange since 2024, under stock code 2533.HK. The Company started with Huashan Series high-computing power platforms for autonomous driving and released Wudang Series cross-domain platforms in 2023 to address more diverse and sophisticated demands for advanced functionalities in smart vehicles, while also beginning to expand into other applications.



Leader of Intelligent Vehicle Computing Chips



Automotive-
grade SoCs for
Smart Vehicles



Autonomous
Driving
Algorithms



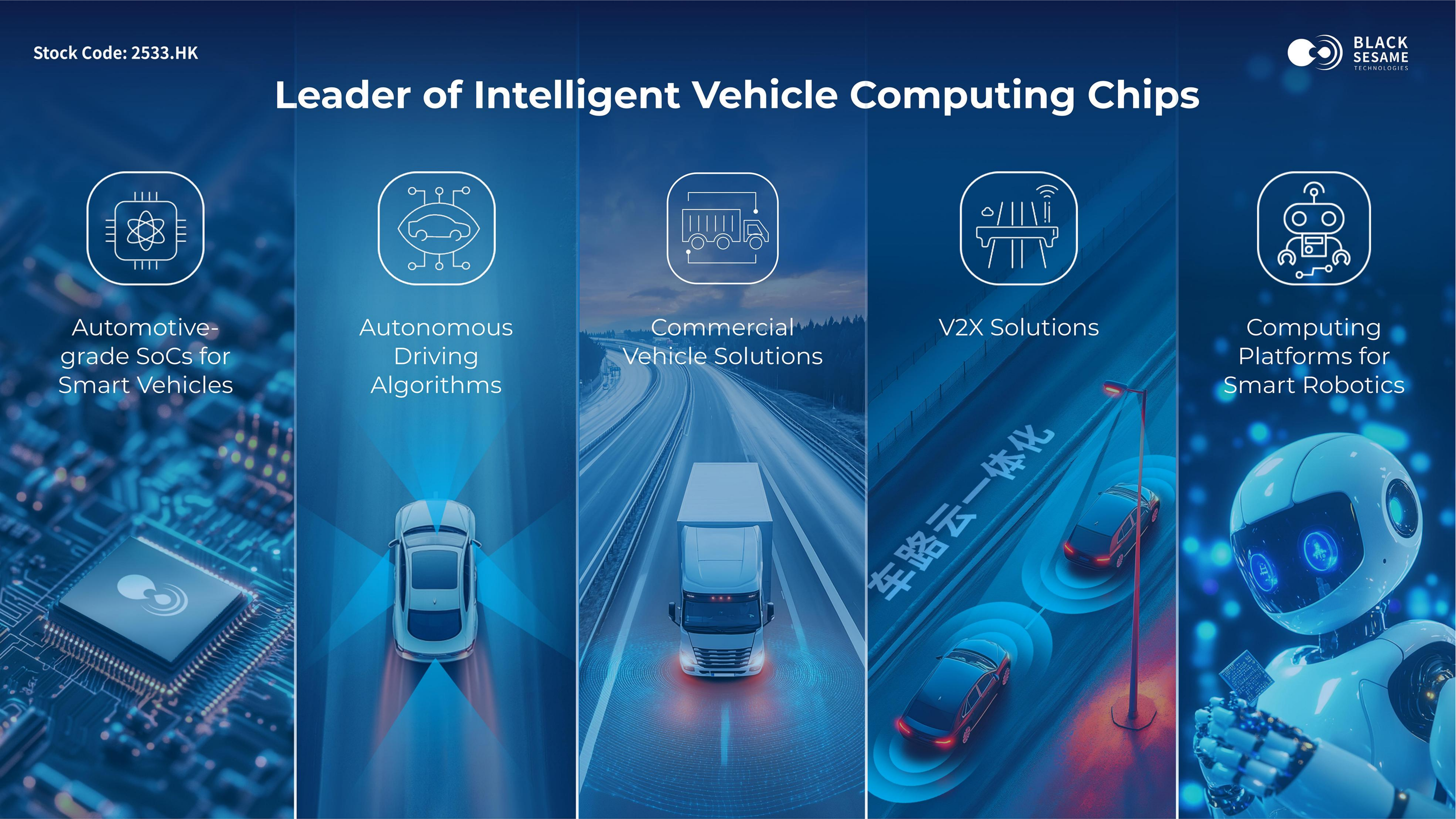
Commercial
Vehicle Solutions



V2X Solutions



Computing
Platforms for
Smart Robotics



International Talents and Global Offices

Having over 1000 employees worldwide, Black Sesame Technologies established R&D and sales centers in the Silicon Valley, Singapore, Hong Kong and mainland China. Our core team consists of talents from top companies like Bosch, OV, Nvidia, Ambarella, Microsoft, Qualcomm, Huawei and ZTE, with averages 15+ years professional experiences.



50+ PhD

| > 85% R&D Team

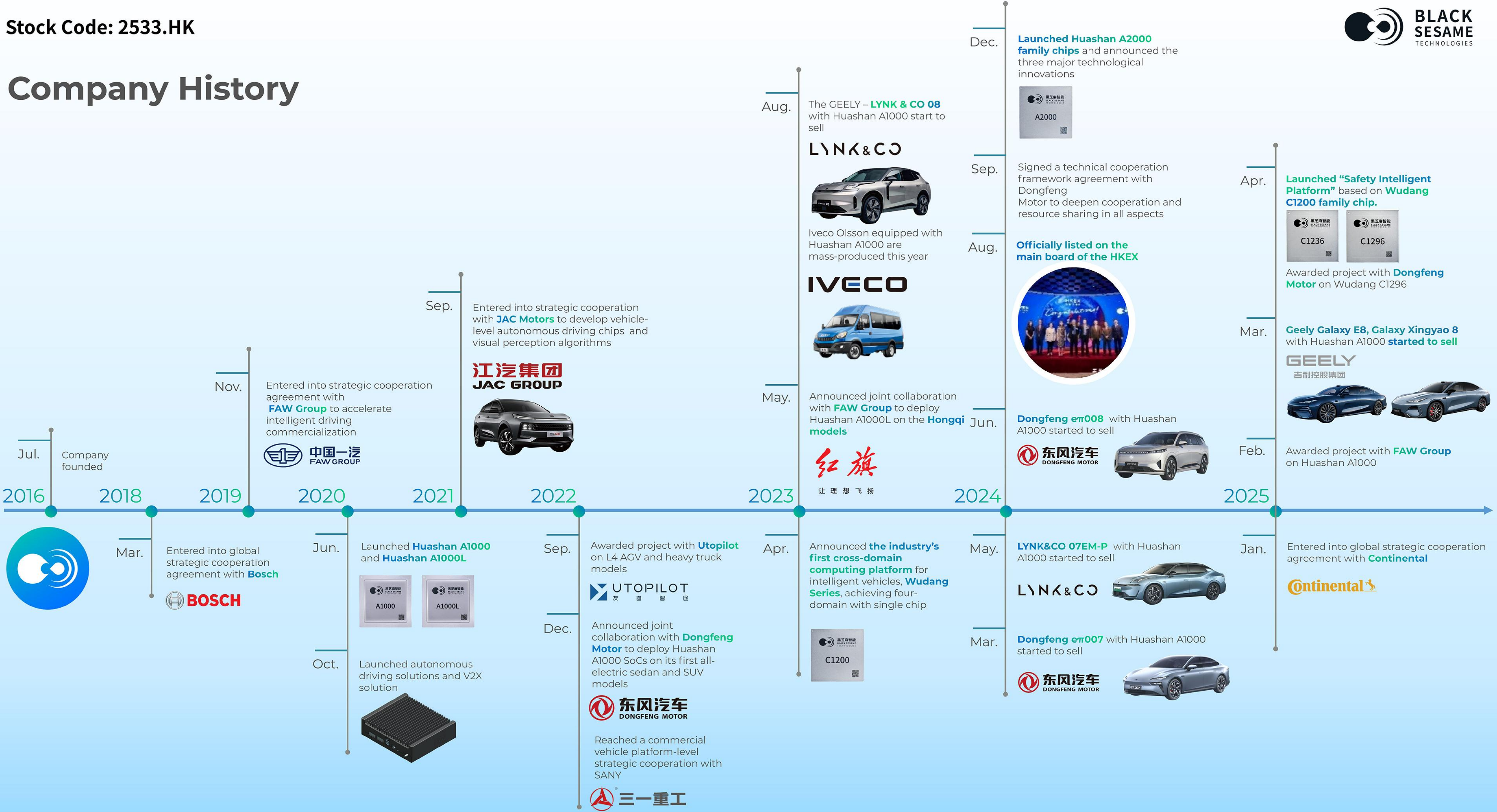
| Avg. 15+ yrs
Dedicated Experience

| Top Computer
Vision Experts
Worldwide

| Solid SoC Design
Capability

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Company History



Qualifications & Honors

As of 2025Q1, Black Sesame Technologies has owned over **160** licensed patents globally and has won over **150** awards from major industry and media.



Products, Technologies and Solutions

Black Sesame Technologies proprietary automotive-grade products and technologies empower intelligent vehicles with mission-critical capabilities, such as autonomous driving, smart cockpit, advanced imaging and interconnection. The company offer full-stack autonomous driving capabilities to meet broad customer needs through SoCs and SoC-based solutions, powered by in-house developed IP cores, algorithms and support software.

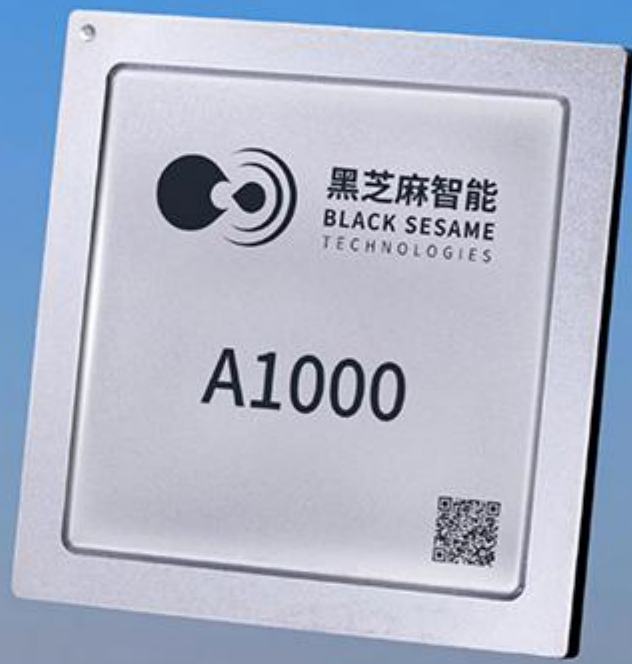


From Autonomous Driving to Cross-domain Fusion

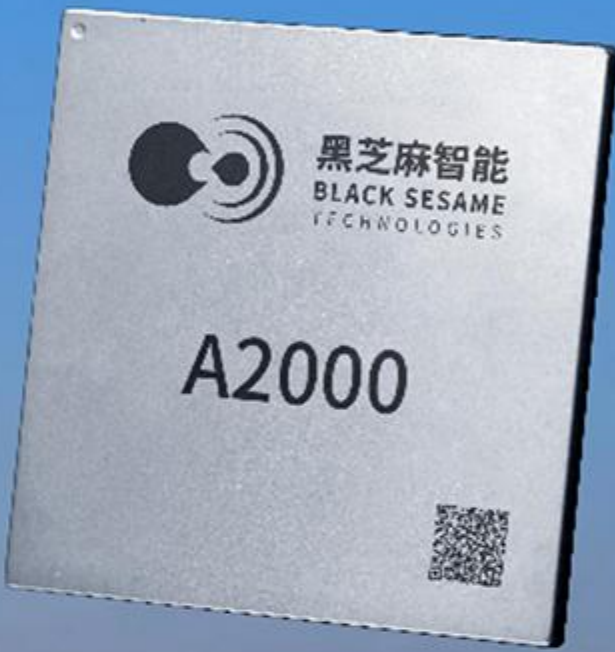
Black Sesame Technologies chip product lines fully support L2-L4 autonomous driving and solutions of cross-domain computing platform in need of different customer requirements.

Huashan Series

High Performance & Cost-effective Autonomous Driving Chip Platform



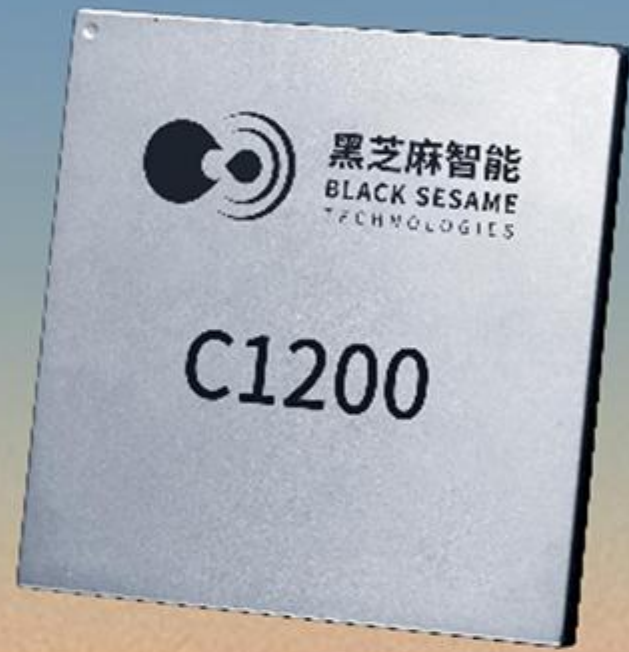
A1000 Family



A2000 Family

Wudang Series

Industry's First Intelligent Vehicle Cross-domain Computing Chip Platform

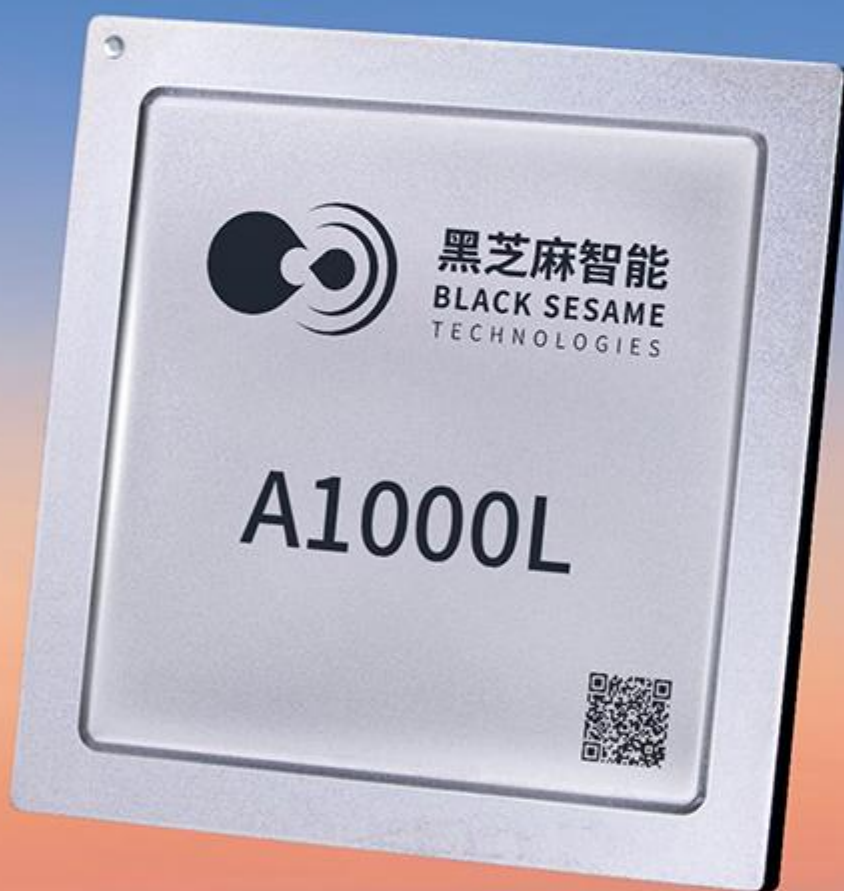


C1200 Family

Product | Huashan Series

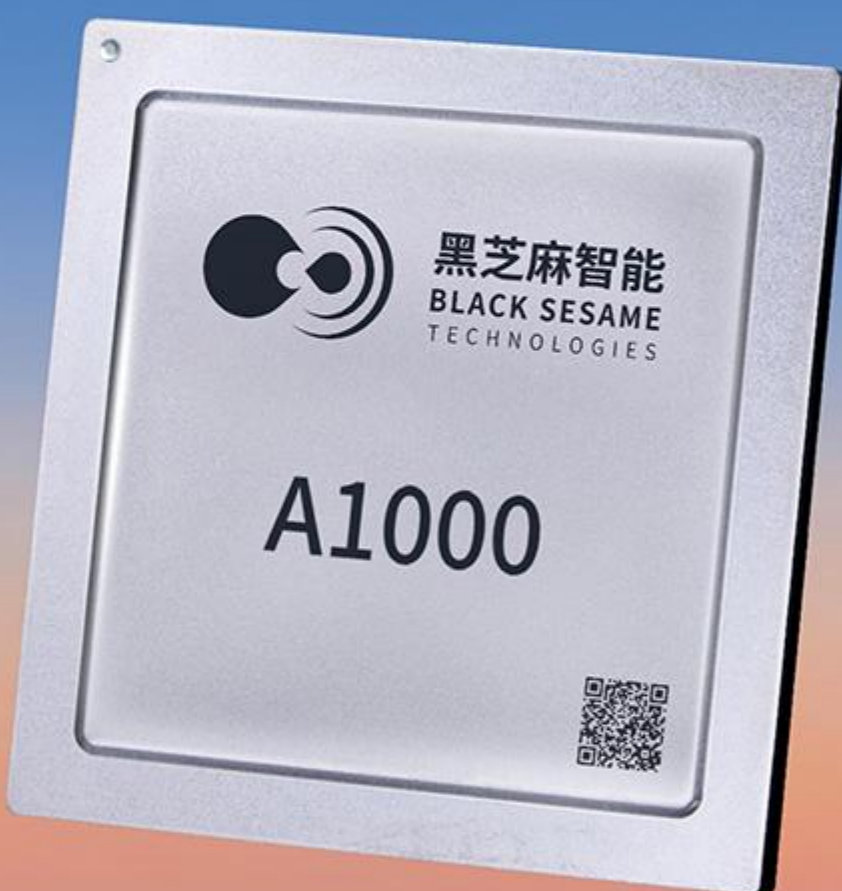
Huashan Series Autonomous Driving Computing Chip

AUTONOMOUS DRIVING



Huashan A1000L
Autonomous Driving Chip

Fully support L2/L2+
autonomous driving



Huashan A1000
Automotive Grade High
Performance AD Chip

Perfectly implement in L2+/
L3 autonomous driving



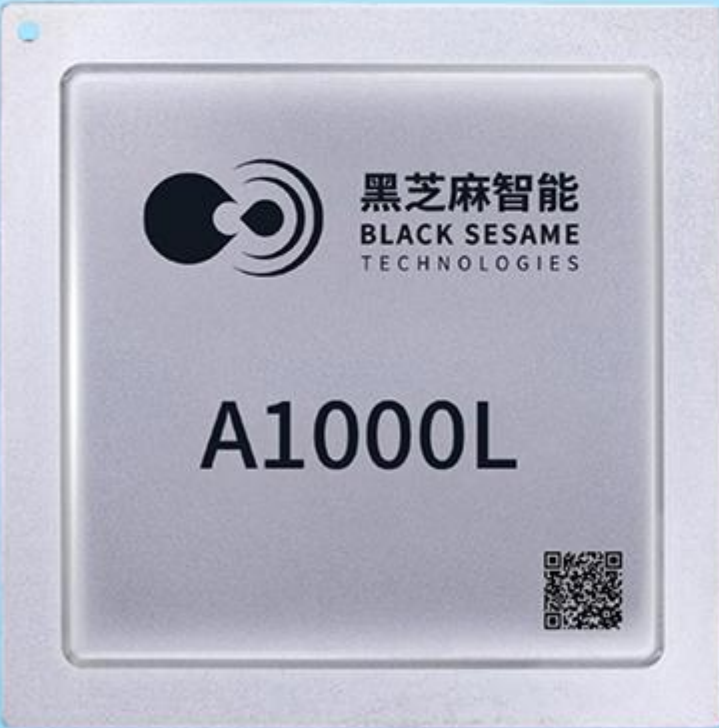
Huashan A2000
Full-scenario Knowledge-
driven AD Chip

New Generation Computing
Platform

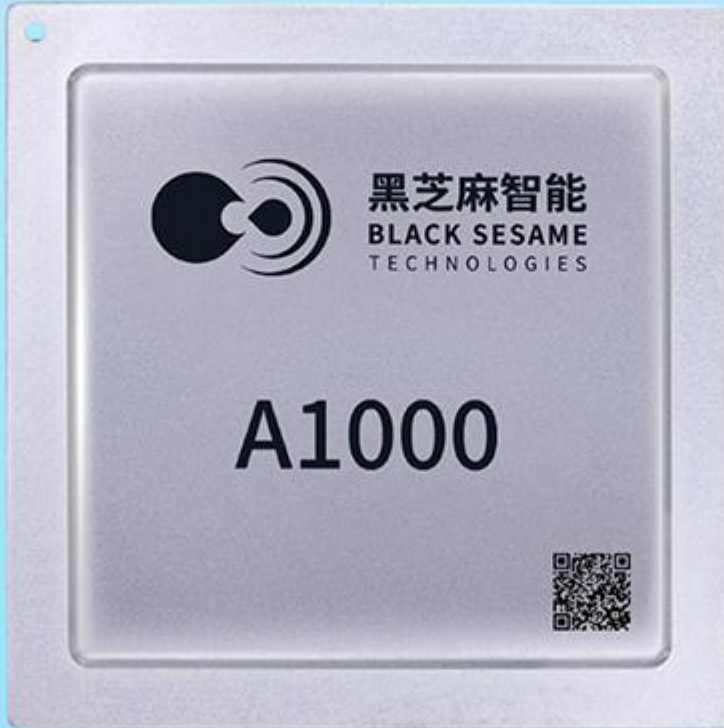
Huashan A1000 Family Chips

A1000 is the high-computing-power platform equipped in the most mass-produced car models in industry, with the highest performance in 16nm auto-grade chips.

A1000L



A1000



LYNK&CO



LYNK&CO 08

东风汽车
DONGFENG MOTOR



Dongfeng eπ008



Dongfeng eπ007

LYNK&CO



LYNK&CO 07

GEELY
吉利控股集团



Geely Xingyao 8



Geely Galaxy E8



2023

2024

2025

2026+

2020

Highlights of Huashan A2000 Family Chips

High Safety & Reliability

Supports redundancy-based Safety NPU

Revolutionary Compute & Memory Architecture

3-level memory hierarchy
Balancing performance, bandwidth, and cost

Native Transformer Support

Comprehensive Product Coverage

From NOA to Robotaxi



New Generation NPU Architecture

High Integrity

6-in-one: CPU+DSP+GPU+NPU+MCU+ISP/CV

New Generation ISP

4-frame exposure, 150dB HDR

Self-Developed High-performance RISC-V Scheduling Core

Flexible Scalability

Supports multi-chip expansion

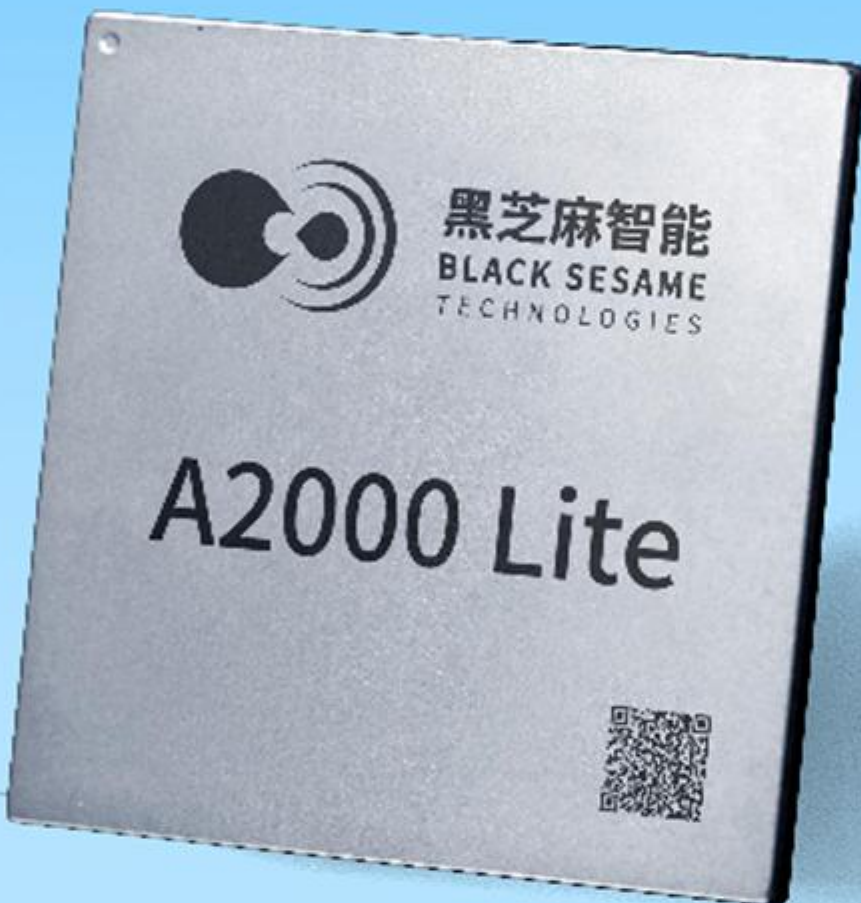
Single-Chip Data Closed-Loop

Huashan A2000 Family Product Portfolio

High-performance Chip Platform Dedicated for Next Generation AI Models

A2000 Lite

Urban Autonomous
Driving Chip



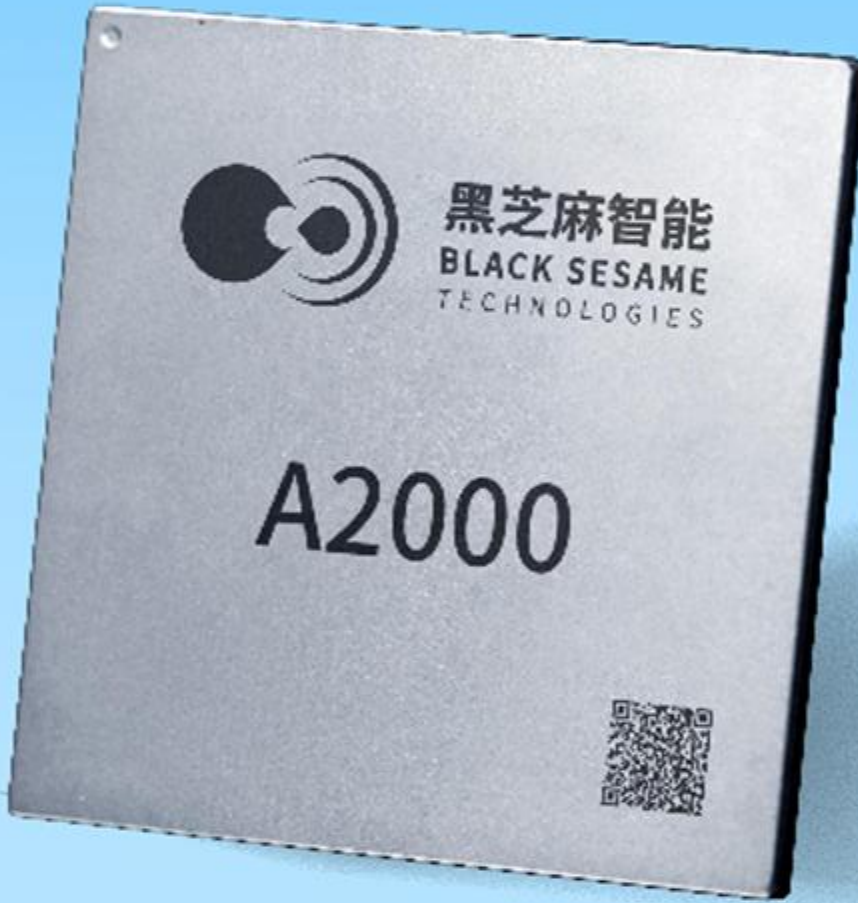
Industry's Extreme
Cost-effectiveness

Typical Solution:
Vision (+ Single LiDAR)
Performance:
1 x Industry flagship chip



A2000

Full-scenario Knowledge-
driven AD Chip



New Generation
Computing Platform

Typical Solution: Vision + Multi-LiDARs
Performance: 2 x industry flagship chip
CPU: 16*Cortex-A78AE
MCU: 6*Cortex-R52(LS)
Automotive-grade IP adopted
Modular design facilitates flexible
expansion



A2000 Pro

High-end Full-scenario
Knowledge-driven AD Chip



Chiplet Architecture Flagship
Computing Platform

Scenario Solution:
Safety and redundancy
Performance:
4 x industry flagship chip



Product | Wudang Series

Wudang Series Cross-domain Computing Chip

CROSS-DOMAIN FUSION



Wudang C1296

The industry's first computing chip to support multi-domain fusion

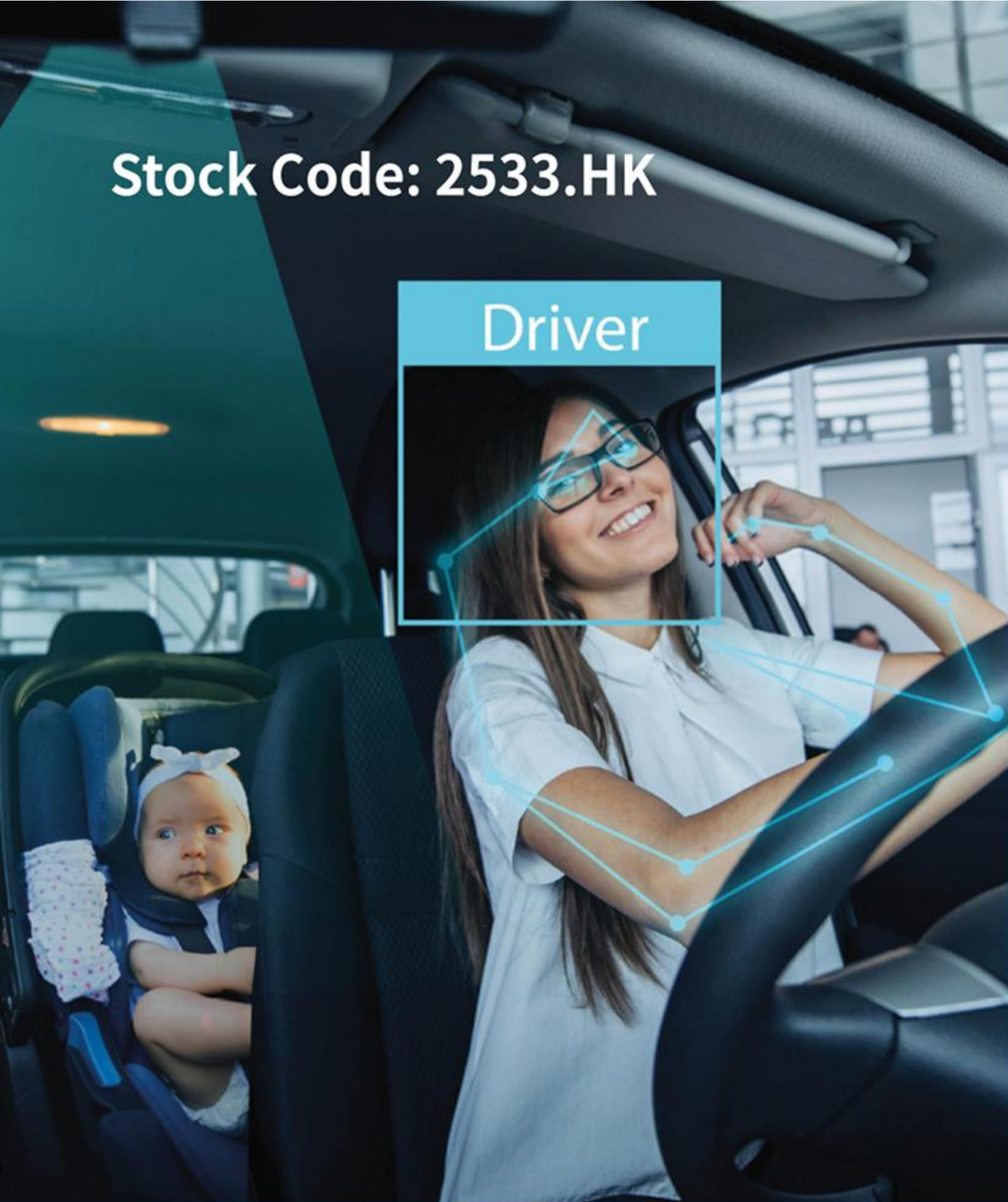


Wudang C1236

The single autonomous driving chip to support NOA driving

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Driver



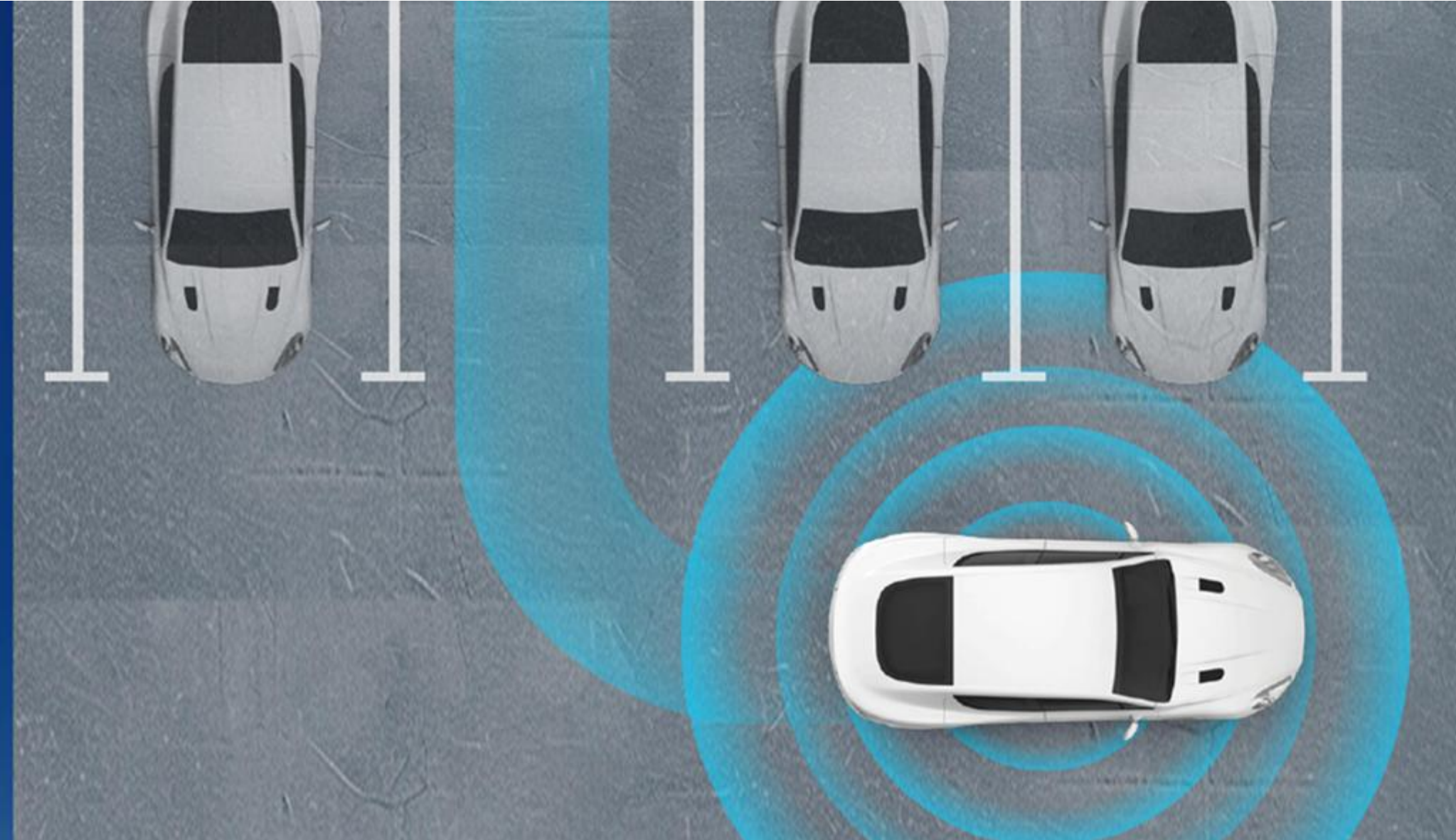
In-cabin sensing system

DMS/OMS
Cabin monitoring
Gesture recognition
Multimodal interaction



CMS system

In-car streaming rear view mirror
Exterior mirror replacement



L2++ driving system

ACC LCA NOA
AEB HWP



Vehicle computing

Data gateway exchange
Body/chassis algorithm
Data security management
SOA service



Wudang C1296

Intelligent headlight

New Scene Interaction
Intelligent headlight control

Safety information system

Digital dashboard
Digital control panel
AR HUD







ISP auto-grade image processing

Imaging · See clear

Our automotive grade ISP provides exceptional image processing capabilities, ensuring that everything on the road is crystal clear.

- > **Matured technology**
- > **Full scenarios covered**
- > **Better cost and power consumption**
- > **Integration of traditional ISP technology and deep learning**



NPU auto-grade deep neural network accelerator

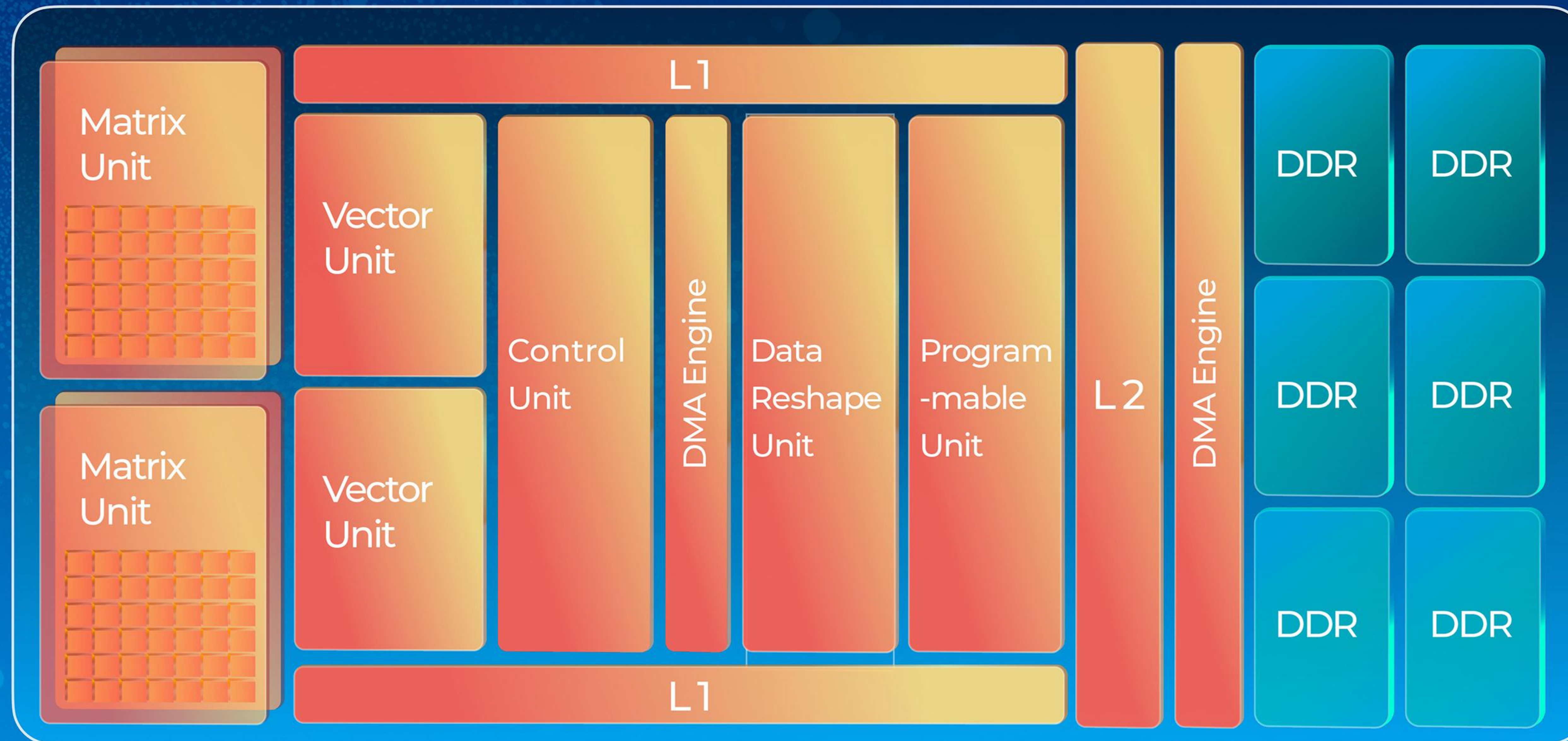
Intelligence · See through

Our automotive grade NPU for deep learning delivers high-performance neural processing capabilities, transforming vehicles into intelligent companions.

- > **Low power consumption and low latency**
- > **High bandwidth data throughput**
- > **Support Transformer**
- > **Accelerator fully decoupled with algorithms**

NPU core — JIUSHAO Architecture

Largest Core × Highest Efficiency



◆ Future-Oriented

Supports a comprehensive mathematical computing architecture that optimizes traditional algorithms and accelerates future evolutionary algorithms

◆ Big-core architecture

Design and integrate the industry's largest independent NPU cores, replacing traditional small core stacks for greater energy efficiency and ease of use

◆ Flexible Expansion

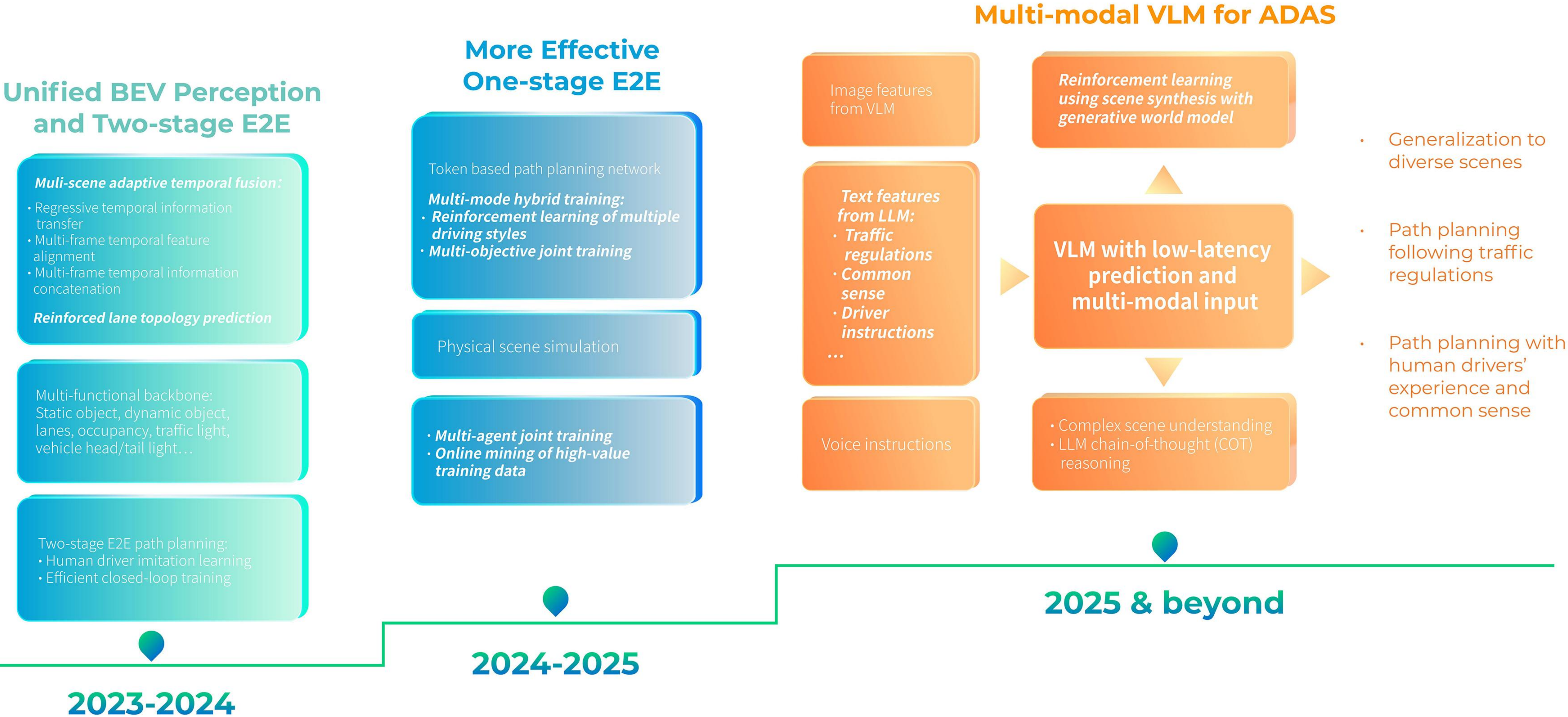
Supports a variety of popular frameworks, different type of precision data, various of network models, and adapted to multiple applications

◆ New Level of Safety

Industry's highest safety-grade NPU core, brings the automotive grade safety to the next level

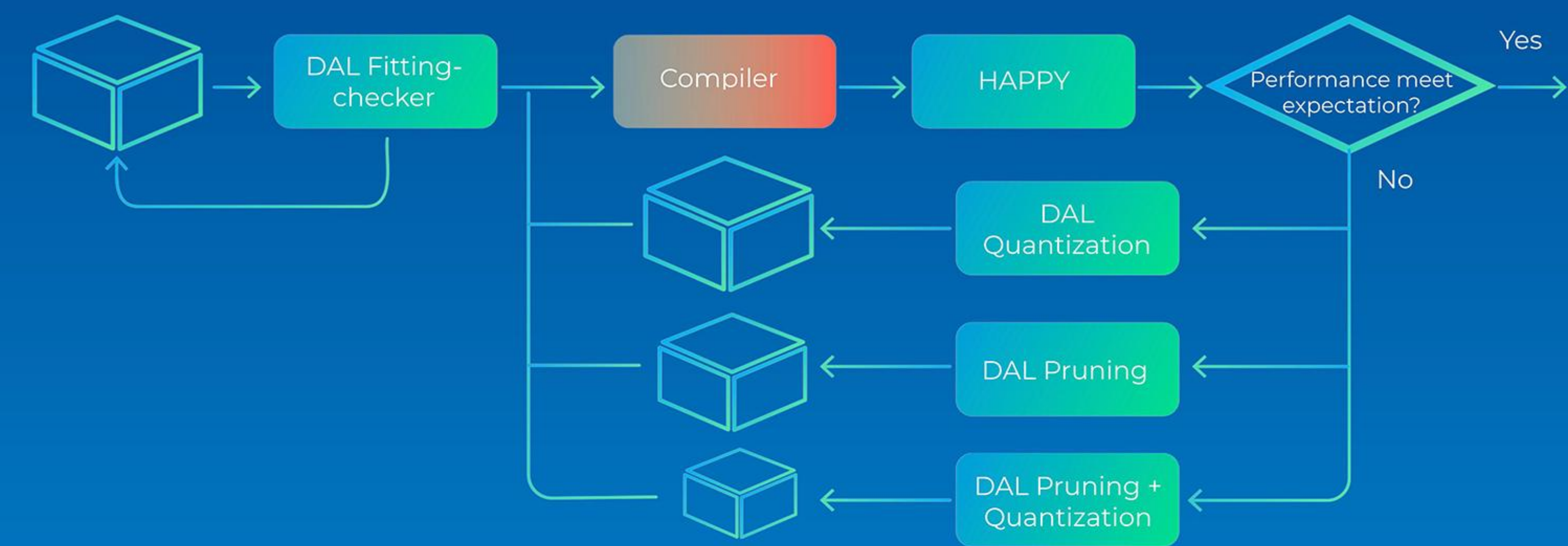
Matured Experience in Perception Algorithms

With the development and validation from CNN to BEV then to E2E algorithms, the company has now entered the R&D stage of autonomous driving algorithms with VLM/VLA and deploying cloud-based world model for reinforcement learning. High level algorithms will **empower autonomous driving system a generalized perception of complex scenarios**, which **makes safer and more reasonable path planning and driving operations** by combining regulations, common sense and plenty of experience.



BaRT | New Generation Universal AI Toolchain

Black Sesame acceleration Run Time



Supports single OS deployment across chips.



Supports high-bandwidth C2C consistency connection, meeting NUMA-style cross-chip memory access requirements.



Shares one set of boot storage devices, reducing costs.

C/C++ perception process

Python perception process

Custom operator libraries

Runtime

C/C++ calls to DLLs

PyTorch runtime support

Triton runtime support

NET Kernel Driver

NET Firmware

Complete Automotive Grade Certifications

Black Sesame Technologies has obtained complete automotive grade certifications:

Automotive Functional Safety Professional

+

Functional Safety Product Certificate

+

Functional Safety Process Certificate

+

Software Functional Safety Process Certificate

+

ASPICE Certificate

+

AEC-Q100 Reliability Certificate



ISO26262 Automotive Functional Safety Professional



ISO26262 ASIL B Functional Safety Product Certificate for A1000



ISO26262 ASIL D Functional Safety Process Certificate



ISO26262 ASIL D Software Functional Safety Process Certificate



ISO9001 Quality Management System



ASPICE CL2 Certificate



AEC-Q100 Reliability Standard Certificate

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Solution

BEST Road - V2X Edge Computing Solution

Solution for V2X scenario applications needs in smart cities



Continuously Improvement in
Computation and Perception
Platform at Roadside

Powerful Real-time Computing Chip

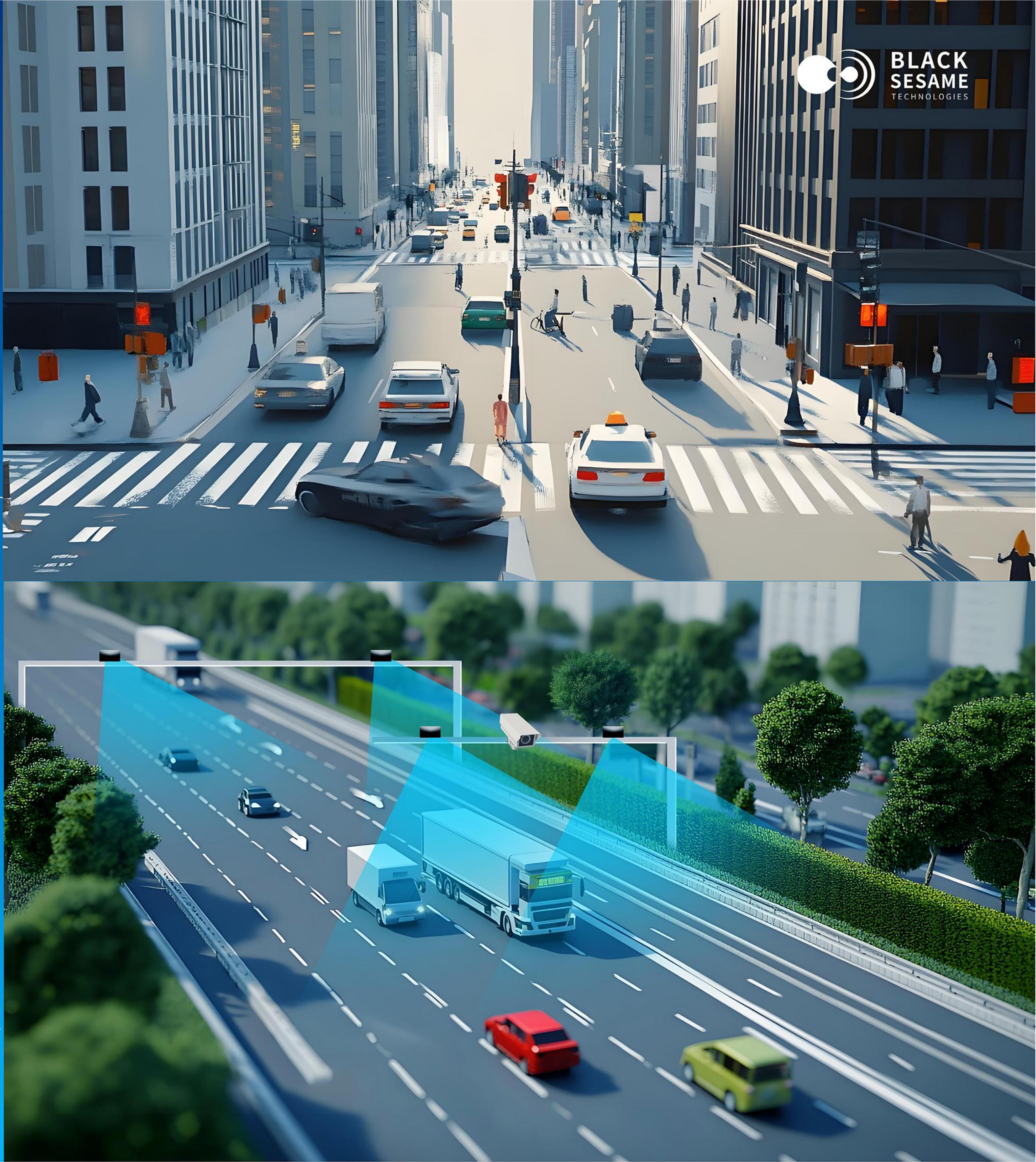
Multi-Scenarios Image Processing

Efficient and Precise Perception Algorithm

Reliable MEC Unit



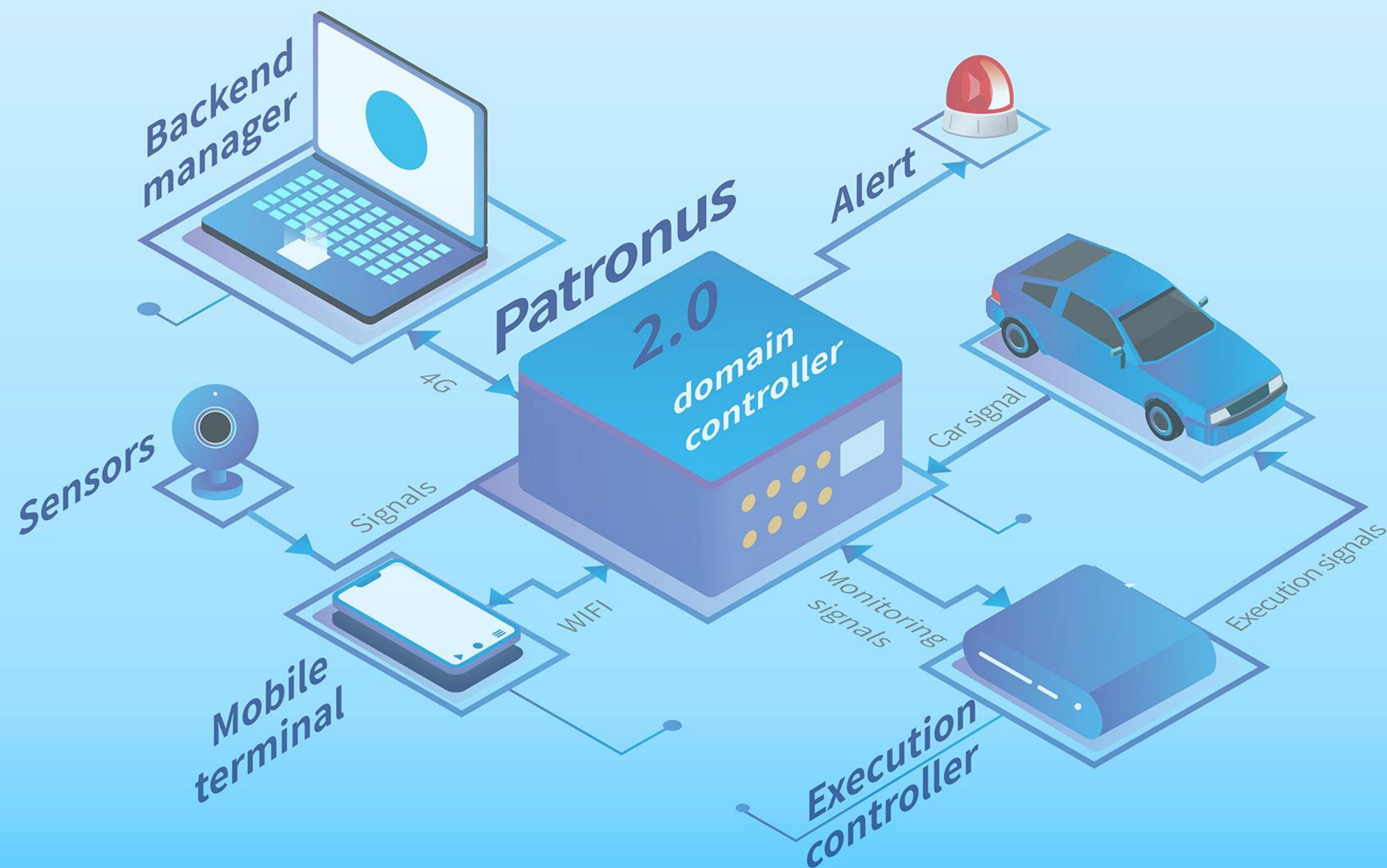
Empowering Smart
City and Smart
Road



Solution

Patronus2.0 - CV Active Safety System

Commercial Vehicle Active Safety System Patronus 2.0 Architecture



Patronus 2.0 – The CV (Commercial Vehicle) Active Safety System, is an aftermarket-mounted integrated solution. This platform can be used in different types of vehicles like commercial vehicles and vans. The main chip of this platform is Black Sesame Technologies’ self-developed AI Huashan Series chip with great computational power, low cost and highly open, which supports massive NN perception, DMS, BSD and BEV algorithms.

ADAS & Recording

Equipped with company's self-developed neural network model, the algorithm has high precision, multiple types of target recognition, and strong scene adaptability; with the actuator can realize the active safety function; reduce hardware costs by sharing DVR cameras..

BSD

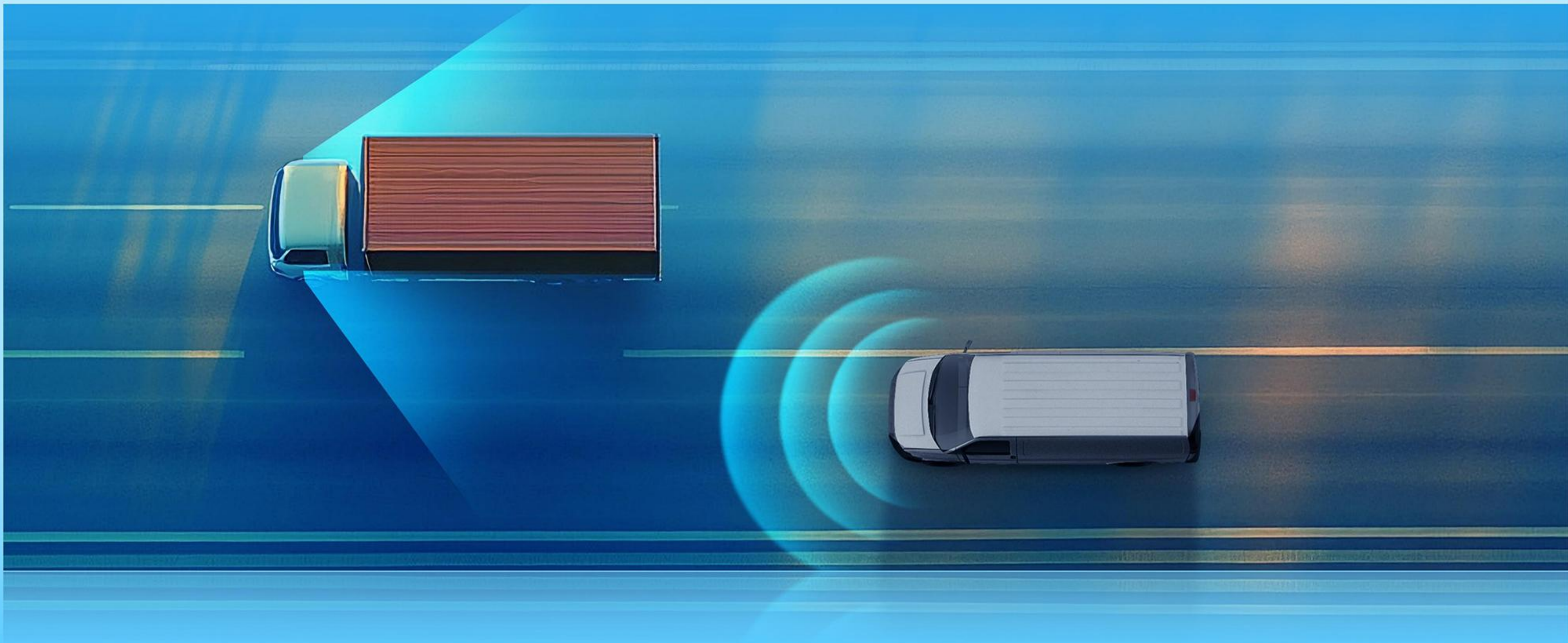
The function realizes identification of vehicles, pedestrians and obstacles in the blind area, and is able to recognize objectives quickly and accurately. And can realize active control functions with the actuator.

DMS

The self-developed algorithm realizes functions like face recognition, monitoring of fatigue, distraction, smoking, phone calling and leaving situations, with low demand for computing power and strong flexibility.

Surround Viewing & Monitoring

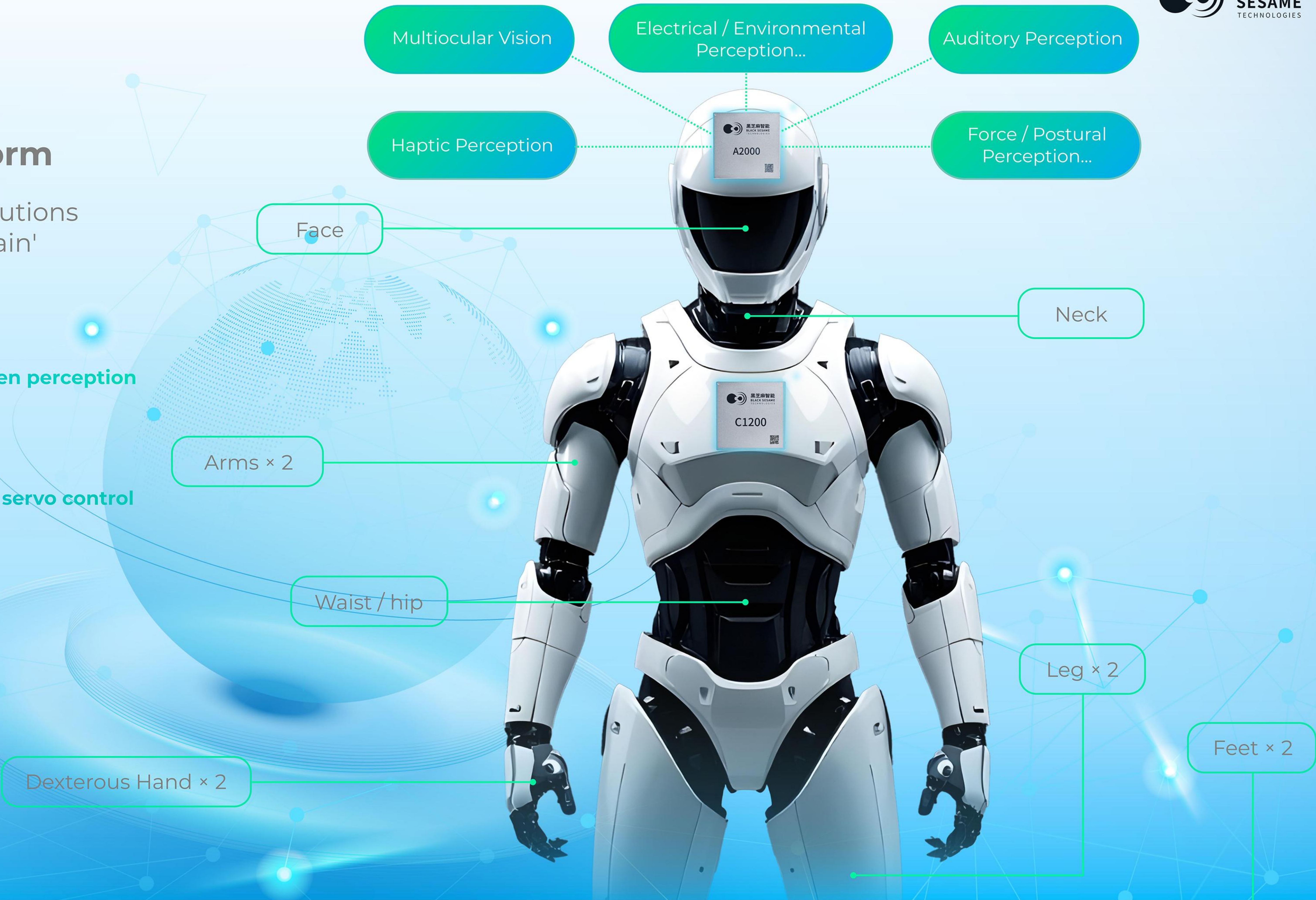
It monitors inside and outside of the cabin for assistance of parking, reversing and in-car monitoring & etc.



Solution
**Intelligent Robot
Computing Platform**

Supports a variety of solutions
from 'cerebellum' to 'brain'

- A2000 Family**
- Full-scenario knowledge-driven perception
and decision-making
- C1200 Family**
- Real-time motion control and servo control



Application Scenarios

Automotive OEM



V2X



Robotics



Commercial Applications



Tier 1



Software Ecosystem



Hardware Ecosystem



* Sort in alphabetical order

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LinkedIn](#)



Black Sesame Technologies
Official Wechat Account



Black Sesame Technologies
Wechat Video Channel

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EMPOWERING **FUTURE MOBILITY** WITH **CHIPS**

